I-2-0162.1US

June 30, 2010

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Our File:

Date:

In the **PATENT APPLICATION** of:

Zeira et al.

Application No.: 09/845,803

Confirmation No.: 3229

Filed:

April 30, 2001

For: DOWNLINK POWER CONTROL FOR MULTIPLE DOWNLINK TIME SLOTS IN TDD COMMUNICATION SYSTEMS

Group:

2472

Examiner:

Raj K. Jain

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Further to Applicants' Duty of Disclosure pursuant to 37 C.F.R. §1.56, Applicants wish to bring to the Examiner's attention the material cited on the enclosed Form PTO-1449.

The listed references include references previously cited in this application.

Newly cited documents are indicated by an asterisk (*) on the enclosed Form PTO
1449. Copies of the newly cited documents are enclosed.

This Application claims priority from Provisional Application No. 60/200,756, which was filed on May 1, 2000.

1320409-1

Applicant: Zeira et al. Application No.: 09/845,803

It is respectfully requested that the Examiner consider these documents and return an initialed copy of the Form PTO-1449 indicating consideration of the cited materials.

Respectfully submitted,

Zeira et al.

DluA - Mutal

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TAM/pck Enclosures (7)